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circuit, the color purity of each of blue luminescence, green luminescence, and red luminescence is suitably controlled in accordance with the voltage and current of the corrected video signal.

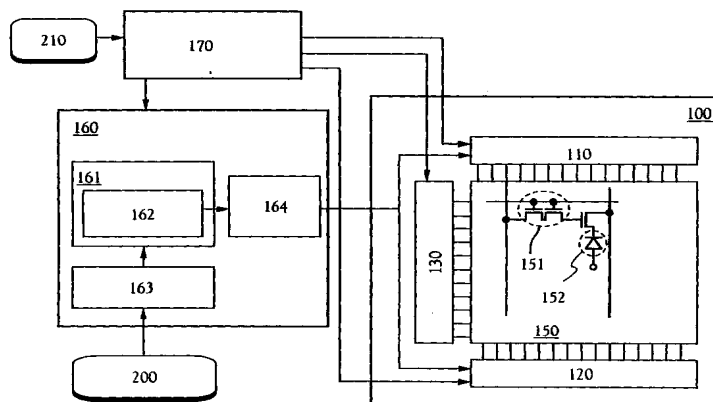


FIG. 1

## Description

### BACKGROUND OF THE INVENTION

#### 1. Field of the Invention

**[0001]** The present invention is related to an EL (electroluminescence) display device formed by a semiconductor element (an element using a semiconductor thin film, typically a thin-film transistor) built on a substrate, and to an electronic device having the EL display device as a display portion.

#### 2. Description of the Related Art

**[0002]** Techniques of forming a thin-film transistor (hereafter referred to as a TFT) on a substrate have greatly advanced in recent years, and development of applications to active matrix display devices is proceeding. In particular, a TFT using a polysilicon film has a higher electric field effect mobility than a TFT using a conventional amorphous silicon film, and therefore it is capable of high speed operation. It therefore becomes possible to perform control of a pixel, conventionally performed by a driver circuit external to the substrate, by a driver circuit formed on the same substrate as the pixel.

**[0003]** This type of active matrix display device is gaining attention for obtaining many advantages, such as lowered manufacturing cost, smaller display device, increased yield, and reduced throughput, by building various circuits and elements on the same substrate.

**[0004]** In addition, research on active matrix EL display devices having EL elements as self-luminescing elements has become spirited. The EL display device is also referred to as organic EL display (OLED) or an organic light emitting diode (OLED).

**[0005]** The EL display device is a self luminescing type which differs from a liquid crystal display device. The EL element has a structure in which an EL layer is sandwiched between a pair of electrodes, and the EL layer is normally a lamination structure. A lamination structure of a hole transporting layer, a light emitting layer, and an electron transporting layer proposed by Tang, et al., of Eastman Kodak Co. can be given as a typical structure. This structure has extremely high light emitting efficiency, and nearly all EL display devices in which research and development is proceeding employ this structure.

**[0006]** Furthermore, additional structures such as a hole injecting layer, a hole transporting layer, a light emitting layer, and an electron transporting layer formed in order on a pixel electrode; or a hole injecting layer, a hole transporting layer, a light emitting layer, an electron transporting layer, and an electron injecting layer formed in order on a pixel electrode may also be used. An element such as a fluorescing pigment may also be doped into the EL layer.

**[0007]** A predetermined voltage is then applied to the EL layer having the above structure by a pair of electrodes, and recombination of a carrier thus occurs in the light emitting layer, and light is emitted.

**[0008]** The EL display device has roughly four colorizing display systems, that is, a system in which an EL element of white luminescence and color filters are combined, a system in which three kinds of EL elements corresponding to R (red), G (green) and B (blue) are formed, a system in which an EL element of blue or blue-green luminescence and a fluorescent material (fluorescent color converting layer: CCM) are combined, and a system in which a transparent electrode is used as a cathode (counter electrode) and EL elements corresponding to RGB are overlapped.

**[0009]** The color filter is a color filter for extracting light of red, green or blue. The color filter is formed at a position corresponding to a pixel, and the color of extracted light at each pixel can be changed by this. This is the same as a colorizing system of a liquid crystal display device using color filters in principal. Note that the position corresponding to the pixel indicates a position coincident with a pixel electrode.

**[0010]** However, the color filter is a filter which extracts the light of a specified wavelength to improve the color purity of the transmitted light. Thus, in the case where a light component of a wavelength to be extracted is small, there can occur such a disadvantage that the brightness of the light of the wavelength is extremely low or the color purity is poor.

**[0011]** In well-known organic EL materials, a red with high luminescent brightness has not been realized, and like an example shown in Fig. 10, the luminescent brightness of red is low as compared with the luminescent brightness of blue or green. In the case where an organic EL material having such luminescent characteristics is used for an EL display device, the luminescent brightness of red of an image to be displayed becomes poor.

**[0012]** Further, a method of using orange color light having a slightly lower wavelength than that of red color light has been conventionally performed because the brightness of red color light is low compared to the brightness of blue and green light. However, in this case the brightness of red color images which are displayed by the EL display device is low, and when display of the red color image is attempted, it is displayed as orange.

### SUMMARY OF THE INVENTION

**[0013]** In consideration of the above, an object of the present invention is to provide an EL display device for displaying an image which has a desirable good balance between the brightnesses of red, blue, and green light in EL elements with different brightness of red, blue and green.

**[0014]** According to an aspect of the present invention, an electronic device includes an EL display device

having a TFT, a pixel electrode electrically connected to the TFT, an EL element with the pixel electrode as a cathode or an anode, and an insulating layer for sealing the EL element; means for applying an analog image signal to the EL element; and means for gamma ( $\gamma$ )-correcting the analog image signal.

**[0015]** The above structure may further include a memory for storing data for gamma ( $\gamma$ ) correction.

**[0016]** According to another aspect of the present invention, an EL display device includes, on a same substrate, a TFT, a pixel electrode electrically connected to the TFT, an EL element with the pixel electrode as a cathode or an anode, an insulating layer for sealing the EL element, means for applying an analog image signal to the EL element, and means for gamma ( $\gamma$ )-correcting the analog image signal.

**[0017]** The above structure may further include a memory for storing data for gamma ( $\gamma$ ) correction on the same substrate.

**[0018]** Besides, in the EL display device, a color filter is formed at a position corresponding to the pixel electrode for colorization.

**[0019]** Besides, for colorization by using another method, the EL element may be formed of a first pixel including a blue luminescent layer, a second pixel including a green luminescent layer, and a third pixel including a red luminescent layer. In this case, the color filter may be used or may not be used.

**[0020]** Besides, in the above EL display device, the gamma ( $\gamma$ ) correction may amplify a signal of red, or may attenuate a signal of blue or green. Besides, the gamma ( $\gamma$ ) correction may be independently made for each of signals of blue, green and red.

**[0021]** By adopting the foregoing structure, even in the case of using an EL material in which a red light component of a wavelength to be extracted by the color filter is small, it is possible to provide an EL display device displaying an image of a desired RGB (red, blue, green) balance by making the gamma ( $\gamma$ ) correction to, for example, a video signal to adjust the luminescent brightness of RGB (red, blue, green).

## BRIEF DESCRIPTION OF THE DRAWINGS

### [0022]

Fig. 1 is a circuit block diagram of an EL display device of the present invention.

Fig. 2 is a structural view when a gamma ( $\gamma$ ) correction table of the EL display device of the present invention is prepared.

Figs. 3A to 3E are views showing a fabricating process of an active matrix EL display device of Embodiment 1.

Figs. 4A to 4D are views showing the fabricating process of the active matrix EL display device of Embodiment 1.

Figs. 5A to 5C are views showing the fabricating

process of the active matrix EL display device of Embodiment 1.

Fig. 6 is a sectional view of an EL display device of Embodiment 1.

Fig. 7 is a top view of an EL display device of Embodiment 1.

Figs. 8A to 8F are views showing examples of electronic instruments of Embodiment 5.

Fig. 9A and 9B are views showing examples of electronic instruments of Embodiment 5.

Fig. 10 is a view showing characteristics between the luminescent brightness of EL elements (R, G, B) and current density.

## DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

[Embodiment-Mode]

**[0023]** First, an embodiment mode of carrying out the present invention will be described with reference to Figs. 1 and 2.

**[0024]** Fig. 1 is a block diagram showing an EL display device of the present invention. In Fig. 1, reference numeral 100 designates an active matrix substrate which includes source driver circuits 110 and 120, a gate driver circuit 130, and a pixel portion 150. The pixel portion 150 includes pixels arranged in matrix form, and each pixel includes a TFT 151, an EL element 152, and the like. Although not shown for simplification, in this embodiment, colorization is realized by using color filters corresponding to R (red), G (green) and B (blue).

**[0025]** Reference numeral 160 designates an image signal processing circuit which includes an A/D conversion circuit 163 for converting an analog signal inputted from the outside into a digital signal, a correction circuit 161 for correcting the digital signal, and a D/A conversion circuit 164 for converting the corrected digital signal into an analog signal. The correction circuit 161 includes a correction memory 162. In the display device of the present invention, a video signal 200 is gamma ( $\gamma$ )-corrected. For example, the video signal 200 is corrected on the basis of a gamma ( $\gamma$ ) correction table stored in the correction memory.

**[0026]** A control circuit 170 controls various signals supplied to the active matrix substrate 100 and the image signal processing circuit 160. A synchronous signal 210 is inputted to the control circuit 170.

**[0027]** The control circuit 170 is a circuit for forming and supplying pulses (start pulse, clock pulse, synchronous signal, etc.) necessary for controlling the source driver circuits 110 and 120, the gate driver circuit 130, the image signal processing circuit 160, and the like on the basis of the synchronous signal 210.

**[0028]** Note that the control circuit 170 repeats an operation (frequency dividing) for counting a previously set count number (frequency division ratio) of clocks, while the inputted synchronous signal 210 is made a

reference and an oscillation clock signal (OSC) outputted from a phase synchronized oscillator is made an original oscillation. The clocks are counted at the same time as this frequency dividing to form a start pulse (S\_SP) supplied to the source driver circuit in the horizontal direction of a screen and a clock pulse (S CK), a start pulse (G\_SP) supplied to the gate driver circuit in the vertical direction of a screen and a clock pulse (G CK), a clock pulse (D CK), and the like. Further, there is also a case where a horizontal synchronous signal (HSY) and a vertical synchronous signal (VSY) are formed.

**[0029]** The image signal processing circuit 160, the control circuit 170, and the like are mounted on a substrate different from the active matrix substrate 100, for example, another printed substrate, and circuits on the substrate and the active matrix substrate 100 are connected through a cable, a flexible wiring plate, and the like. Note that it is needless to say that it is preferable to make such a structure that a part or all of circuits such as the image signal processing circuit 160, the control circuit 170, and the like are provided on the same substrate as the active matrix substrate since integration and miniaturization are realized.

**[0030]** The video signal 200 inputted to the image signal processing circuit 160 from the outside is an analog signal. The video signal 200 may be an analog signal such as a television signal or a video signal, or may be an analog signal formed by D/A conversion of a data signal from a computer or the like.

**[0031]** In the image signal processing circuit 160, the video signal 200 is converted into a digital video signal by the A/D conversion circuit 163 and is outputted to the correction circuit 161. The correction circuit 161 makes gamma ( $\gamma$ ) correction, in view of the luminescent brightness of each EL element, to the inputted digital video signal on the basis of the gamma ( $\gamma$ ) correction table stored in the correction memory.

**[0032]** In the gamma ( $\gamma$ ) correction, a supplied image signal is corrected in order to obtain excellent gradation display. The gamma ( $\gamma$ )-corrected digital video signal is converted into an analog video signal by the D/A conversion circuit 164 and is supplied to the source driver circuits 110 and 120.

**[0033]** By this correction circuit 161, a video signal supplied to each EL element is gamma ( $\gamma$ )-corrected, and the luminescent brightness of each of blue luminescence, green luminescence and red luminescence can be suitably controlled in accordance with the voltage and current of the corrected analog video signal. For example, in the case where an EL element using three kinds (R, G, B) of color filters as shown in Fig. 10 is used, the video signal (corresponding to R) has only to be gamma ( $\gamma$ )-corrected to increase the luminescent brightness of R so that the luminescent brightness of each color becomes identical. Alternatively, the video signal applied to the EL element (corresponding to B or G) has only to be gamma ( $\gamma$ )-corrected to decrease the

luminescent brightness of B or G so that the luminescent brightness of each color becomes identical. In addition, the video signals applied to the respective EL elements may be gamma ( $\gamma$ )-corrected to increase the luminescent brightness of R and to decrease the luminescent brightness of B or G so that the luminescent brightness of each color becomes identical.

**[0034]** Here, an example of a preparation method of the gamma ( $\gamma$ ) correction table of the correction memory in the correction circuit of the image signal processing circuit 160 of the present invention will be described.

**[0035]** Reference will be made to Fig. 2. Fig. 2 is a circuit block diagram in the case where the gamma ( $\gamma$ ) correction table of the correction memory in the correction circuit of the image signal processing circuit 160 of the present invention is prepared. Reference numeral 201 designates an image pickup device which converts an image displayed by the luminescence of the EL element into an electric signal.

**[0036]** As the image pickup device 201, another image pickup device such as a CCD camera or a digital video camera can be used. Alternatively, a luminance meter or an illuminance meter for merely measuring the lightness or brightness of a displayed image may be used. In the case where the luminance meter or the illuminance meter is used, it is appropriate that an A/D conversion circuit for converting a signal supplied from these devices into a digital signal is used.

**[0037]** Reference numeral 202 designates a digital signal processor (DSP); 203, a reference signal supply source; and 204, a signal generator (SG).

**[0038]** The correction circuit 161 of the image signal processing circuit 160 gamma ( $\gamma$ )-corrects a digital signal supplied from the signal generator 204, outputs a digital video signal after correction, converts the signal into an analog video signal by the D/A conversion circuit, and sends it to the respective EL elements. The respective EL elements emit light on the basis of the analog video signal supplied from the image signal processing circuit 160 and display an image.

**[0039]** The displayed image is made a digital signal by using the image pickup device 201. The digital signal sent from the image pickup device 200 is supplied to the digital signal processor (DSP) 202. The digital signal processor 202 compares the digital signal supplied from the image pickup device 201 with a digital signal supplied from the reference data supply source 203, and feeds back the discrepancy of the data to the correction circuit 161. Note that the reference data may be directly supplied from the signal generator 204.

**[0040]** In accordance with the signal supplied from the digital signal processor 202, the correction circuit 161 further corrects the digital signal from the signal generator 204, converts it into an analog video signal, and again sends it to the EL element. The respective EL elements emit light on the basis of the analog video signal supplied from the image signal processing circuit 160 and display an image.

**[0041]** The displayed image is again made a digital signal by using the image pickup device 201. The digital signal supplied from the image pickup device 201 is sent to the digital signal processor 202. The digital signal processor 202 compares the digital signal supplied from the image pickup device 201 with the digital signal supplied from the reference data supply source 203, and feeds back the discrepancy to the correction circuit 161.

**[0042]** When suitable data of gamma ( $\gamma$ ) correction are obtained in this way, the data are stored at an appointed address of the correction memory 162.

**[0043]** Thereafter, in order to start the correction of a next video signal, the signal generator 204 sends a digital signal different from the previous one to the correction circuit 161. When suitable data of gamma ( $\gamma$ ) correction to the digital signal are obtained, the data are stored at an appointed address of the correction memory 162.

**[0044]** When all correction data are stored in the correction memory 162, the signal generator 204 and the digital signal processor 202 are separated from the active matrix substrate 100. Here, the preparation of the gamma ( $\gamma$ ) correction table is completed. Note that the method of preparing the gamma ( $\gamma$ ) correction table shown here is merely an example, and it is needless to say that the present invention is not particularly limited. Besides, the block circuit diagram of Fig. 1 is also an example, and it is also possible to make gamma ( $\gamma$ ) correction by using, for example, a correction circuit without a correction memory.

**[0045]** Thereafter, a digital video signal is supplied to the correction circuit 160, and on the basis of the data of the gamma ( $\gamma$ ) correction table stored in the correction memory 161, the digital video signal is corrected, and after the signal is further converted into an analog video signal, it is supplied to the EL element. Since the suitable correction is made to the analog video signal supplied to the EL element by the correction circuit 160, balanced luminescence (red luminescence, green luminescence, and blue luminescence) is obtained and an excellent image is displayed.

**[0046]** The present invention having the above structure will be described in more detail with reference to the following embodiments.

#### [Embodiment 1]

**[0047]** In this embodiment, an EL display device provided with a correction circuit will be described with reference to Fig. 1.

**[0048]** Fig. 1 is a block diagram showing an EL display device of this embodiment. In Fig. 1, reference numeral 100 designates an active matrix substrate which includes source driver circuits 110 and 120, a gate driver circuit 130, and a pixel portion 150. The pixel portion 150 includes pixels arranged in matrix form, and each pixel includes a TFT 151, an EL element 152, and

the like. Although not shown for simplification, in this embodiment, colorization is realized by using color filters corresponding to R (red), G (green) and B (blue).

**[0049]** Reference numeral 160 designates an image signal processing circuit which includes an A/D conversion circuit 163 for converting an analog signal inputted from the outside into a digital signal, a correction circuit 161 for gamma ( $\gamma$ )-correcting the digital signal, and a D/A conversion circuit 164 for converting the gamma ( $\gamma$ )-corrected digital signal into an analog signal. The correction circuit 161 includes a correction memory 162.

**[0050]** Reference numeral 170 designates a control circuit which controls various signals supplied to the active matrix substrate 100 and the image signal processing circuit 160. A synchronous signal 210 is inputted to the control circuit 170.

**[0051]** The image signal processing circuit 160, the control circuit 170, and the like are mounted on a substrate different from the active matrix substrate 100, for example, another printed substrate, and circuits on the substrate and the active matrix substrate 100 are connected through a cable, a flexible wiring plate, and the like.

**[0052]** A video signal 200 inputted to the image signal processing circuit 160 from the outside is an analog signal such as a television signal or a video signal.

**[0053]** In the image signal processing circuit 160, the video signal 200 is converted into a digital video signal by the A/D conversion circuit 163 and is outputted to the correction circuit 161. The correction circuit 161 makes gamma ( $\gamma$ ) correction, in view of the luminescent brightness of each EL element, to the inputted digital video signal on the basis of the gamma ( $\gamma$ ) correction table stored in the correction memory. The gamma ( $\gamma$ )-corrected digital video signal is converted into an analog video signal by the D/A conversion circuit 164 and is supplied to the source driver circuits 110 and 120.

**[0054]** The digital video signal is supplied to the correction circuit 160, and on the basis of the data of the gamma ( $\gamma$ ) correction table stored in the correction memory 161, the digital video signal is gamma ( $\gamma$ )-corrected, and after the signal is further converted into an analog video signal, it is supplied to the EL element. Since the suitable gamma ( $\gamma$ ) correction is made to the analog video signal supplied to the EL element, balanced luminescence (red luminescence, green luminescence, and blue luminescence) is obtained and an excellent image is displayed.

**[0055]** The fabricating method of the EL display device in this embodiment of the present invention are explained using Figs. 3A to 5C. Note that in order to simplify the explanation, a CMOS circuit is shown as a basic circuit for the driver circuits.

**[0056]** First, as shown in Fig. 3A, a base film 301 is formed with a 300 nm thickness on a glass substrate 300. Silicon nitride oxide films are laminated as the base film 302 in embodiment 1. It is good to set the

nitrogen concentration at between 10 and 25 wt% in the film contacting the glass substrate 300.

**[0057]** Next, an amorphous silicon film (not shown in the figures) is formed with a thickness of 50 nm on the base film 301 by a known deposition method. Note that it is not necessary to limit this to the amorphous silicon film, and another film may be formed provided that it is a semiconductor film containing an amorphous structure (including a microcrystalline semiconductor film). In addition, a compound semiconductor film containing an amorphous structure, such as an amorphous silicon germanium film, may also be used. Further, the film thickness may be made from 20 to 100 nm.

**[0058]** The amorphous silicon film is then crystallized by a known technique, forming a crystalline silicon film (also referred to as a polycrystalline silicon film or a polysilicon film) 302. Thermal crystallization using an electric furnace, laser annealing crystallization using a laser, and lamp annealing crystallization using an infrared light exist as known crystallization methods. Crystallization is performed in embodiment 1 using light from an excimer laser which uses XeCl gas.

**[0059]** Note that pulse emission type excimer laser light formed into a linear shape is used in embodiment 1, but a rectangular shape may also be used, and continuous emission argon laser light and continuous emission excimer laser light can also be used.

**[0060]** In this embodiment, although the crystalline silicon film is used as the active layer of the TFT, it is also possible to use an amorphous silicon film. Note that it is possible to form the active layer of the switching TFT, in which there is a necessity to reduce the off current, by the amorphous silicon film, and to form the active layer of the current control TFT by the crystalline silicon film. Electric current flows with difficulty in the amorphous silicon film because the carrier mobility is low, and the off current does not easily flow. In other words, the most can be made of the advantages of both the amorphous silicon film, through which current does not flow easily, and the crystalline silicon film, through which current easily flows.

**[0061]** Next, as shown in Fig. 3B, a protecting film 303 is formed on the crystalline silicon film 302 with a silicon oxide film having a thickness of 130 nm. This thickness may be chosen within the range of 100 to 200 nm (preferably between 130 and 170 nm). Furthermore, other films may also be used providing that they are insulating films containing silicon. The protecting film 303 is provided so that the crystalline silicon film is not directly exposed to plasma during addition of an impurity, and so that delicate concentration control of the impurity is possible.

**[0062]** Resist masks 304a and 304b are then formed on the protecting film 303, and an impurity element which imparts n-type conductivity (hereafter referred to as an n-type impurity element) is added. Note that elements residing in periodic table group 15 are generally used as the n-type impurity element, and

typically phosphorous or arsenic can be used. Note that a plasma doping method is used, in which phosphine ( $\text{PH}_3$ ) is plasma activated without separation of mass, and phosphorous is added at a concentration of  $1 \times 10^{18}$  atoms/cm<sup>3</sup> in embodiment 1. Of course, an ion implantation method, in which separation of mass is performed, may also be used.

**[0063]** The dose amount is regulated so that the n-type impurity element is contained in n-type impurity regions 305 and 306, thus formed by this process, at a concentration of  $2 \times 10^{16}$  to  $5 \times 10^{19}$  atoms/cm<sup>3</sup> (typically between  $5 \times 10^{17}$  and  $5 \times 10^{18}$  atoms/cm<sup>3</sup>).

**[0064]** Next, as shown in Fig. 3C, the protecting film 303 is removed, and an activation of the added periodic table group 15 elements is performed. A known technique of activation may be used as the means of activation, and activation is done in embodiment 1 by irradiation of excimer laser light. A pulse emission type excimer laser and a continuous emission type excimer laser may both, of course, be used, and it is not necessary to place any limits on the use of excimer laser light. The object is the activation of the added impurity element, and it is preferable that irradiation is performed at an energy level at which the crystalline silicon film does not melt. Note that the laser irradiation may also be performed with the protecting film 303 in place.

**[0065]** The activation by heat treatment may also be performed along with activation of the impurity element by laser light. When activation is performed by heat treatment, considering the heat resistance of the substrate, it is good to perform heat treatment on the order of 450 to 550°C.

**[0066]** A boundary portion (connecting portion) with regions along the edges of the n-type impurity regions 305 and 306, namely regions along the periphery into which the n-type impurity element, which exists in the n-type impurity regions 305 and 306, is not added, is delineated by this process. This means that, at the point when the TFTs are later completed, extremely good connections can be formed between LDD regions and channel forming regions.

**[0067]** Unnecessary portions of the crystalline silicon film are removed next, as shown in Fig. 3D, and island shape semiconductor films (hereafter referred to as active layers) 307 to 310 are formed.

**[0068]** Then, as shown in Fig. 3E, a gate insulating film 311 is formed, covering the active layers 307 to 310. An insulating film containing silicon and with a thickness of 10 to 200 nm, preferably between 50 and 150 nm, may be used as the gate insulating film 311. A single layer structure or a lamination structure may be used. A 110 nm thick silicon nitride oxide film is used in embodiment 1.

**[0069]** Next, a conductive film having a thickness of 200 to 400 nm is formed, and patterning is carried out to form gate electrodes 312 to 316. The end portions of the gate electrodes 312 to 316 can also be made tapered. Note that in this embodiment, the gate elec-

trode and an extended wiring line (hereinafter referred to as a gate wiring line) electrically connected to the gate electrode are formed of different materials. Specifically, a material having a resistance lower than the gate electrode is used for the gate wiring line. This is because a material which can be finely worked is used for the gate electrode and a material which has a low wiring resistance though fine working can not be made is used for the gate wiring line. Of course, the gate electrode and the gate wiring line may be formed of the same material.

**[0070]** Besides, although the gate electrode may be formed of a conductive film of a single layer, it is preferable to make a laminate film such as a two-layer or three-layer film as the need arises. As the material of the gate electrode, any well-known conductive films may be used. However, as described above, it is preferable to use a material which can be finely worked, specifically, can be patterned into a line width of 2  $\mu\text{m}$  or less.

**[0071]** Typically, it is possible to use a film made of an element selected from tantalum (Ta), titanium (Ti), molybdenum (Mo), tungsten (W), chromium (Cr), and silicon (Si), a film of nitride of the above element (typically a tantalum nitride film, tungsten nitride film, or titanium nitride film), an alloy film of combination of the above elements (typically Mo-W alloy, Mo-Ta alloy), or a suicide film of the above element (typically a tungsten suicide film, titanium silicide film). Of course, the films may be used as a single layer or a laminate layer.

**[0072]** In this embodiment, a laminate film of a tungsten nitride (WN) film having a thickness of 50 nm and a tungsten (W) film having a thickness of 350 nm is used. These may be formed by a sputtering method. When an inert gas of Xe, Ne or the like is added as a sputtering gas, film peeling due to stress can be prevented.

**[0073]** The gate electrodes 313 and 316 are formed at this time so as to overlap a portion of the n-type impurity regions 305 and 306, respectively, sandwiching the gate insulating film 311. This overlapping portion later becomes an LDD region overlapping the gate electrode.

**[0074]** Next, an n-type impurity element (phosphorous is used in embodiment 1) is added in a self-aligning manner with the gate electrodes 312 to 316 as masks, as shown in Fig. 4A. The addition is regulated so that phosphorous is added to impurity regions 317 to 323 thus formed at a concentration of 1/2 to 1/10 that of the impurity regions 305 and 306 (typically between 1/3 and 1/4). Specifically, a concentration of  $1 \times 10^{16}$  to  $5 \times 10^{18}$  atoms/cm<sup>3</sup> (typically  $3 \times 10^{17}$  to  $3 \times 10^{18}$  atoms/cm<sup>3</sup>) is preferable.

**[0075]** Resist masks 324a to 324c are formed next, with a shape covering the gate electrodes etc., as shown in Fig. 4B, and an n-type impurity element (phosphorous is used in embodiment 1) is added, forming impurity regions 325 to 331 containing a high concentration of phosphorous. Ion doping using phosphine (PH<sub>3</sub>) is also performed here, and is regulated so that

the phosphorous concentration of these regions is from  $1 \times 10^{20}$  to  $1 \times 10^{21}$  atoms/cm<sup>3</sup> (typically between  $2 \times 10^{20}$  and  $5 \times 10^{21}$  atoms/cm<sup>3</sup>).

**[0076]** A source region or a drain region of the n-channel TFT is formed by this process, and in the switching TFT, a portion of the n-type impurity regions 320 to 322 formed by the process of Fig. 4A remains.

**[0077]** Next, as shown in Fig. 4C, the resist masks 324a to 324c are removed, and a new resist mask 332 is formed. A p-type impurity element (boron is used in embodiment 1) is then added, forming impurity regions 333 and 334 containing a high concentration of boron. Boron is added here at a concentration of  $3 \times 10^{20}$  to  $3 \times 10^{21}$  atoms/cm<sup>3</sup> (typically between  $5 \times 10^{20}$  and  $1 \times 10^{21}$  atoms/cm<sup>3</sup>) by ion doping using diborane (B<sub>2</sub>H<sub>6</sub>).

**[0078]** Note that phosphorous has already been added to the impurity regions 333 and 334 at a concentration of  $1 \times 10^{20}$  to  $1 \times 10^{21}$  atoms/cm<sup>3</sup>, but boron is added here at a concentration of at least 3 times more than that of the phosphorous. Therefore, the n-type impurity regions already formed completely invert to p-type, and function as p-type impurity regions.

**[0079]** Next, after the resist mask 332 is removed, the n-type or p-type impurity element added at each concentration is activated. As activating means, a furnace annealing method, a laser annealing method, or a lamp annealing method can be used. In this embodiment, a heat treatment in a nitrogen atmosphere at 550°C for 4 hours is carried out in an electric heating furnace.

**[0080]** At this time, it is important to remove oxygen in the atmosphere to the utmost. This is because if oxygen exists even in a small amount, the exposed surface of the gate electrode is oxidized to cause the increase of resistance, and also it becomes difficult to make ohmic contact later. Thus, it is desirable that an oxygen concentration in the treatment atmosphere at the activation step is made 1 ppm or less, preferably 0.1 ppm or less.

**[0081]** Next, after the activation step is ended, a gate wiring line 335 having a thickness of 300 nm is formed. As a material of the gate wiring line 335, a metal film containing aluminum (Al) or copper (Cu) as its main ingredient (constituting 50 to 100%) may be used. As an arrangement, it is formed so as to electrically connect the gate electrodes 314 and 315 of the switching TFT (Fig. 4D).

**[0082]** By adopting such structure, the wiring resistance of the gate wiring line can be made very low, so that an image display region (pixel portion) with a large area can be formed. That is, the pixel structure of this embodiment is very effective in realization of an EL display device having a screen of a size of not less than 10 inches in diagonal (further, 30 cm or more).

**[0083]** Next, as shown in Fig. 5A, a first interlayer insulating film 336 is formed. As the first interlayer insulating film 336, a single layer of an insulating film containing silicon may be used, or a laminate film of a

combination of such films may be used. Besides, it is appropriate that the film thickness is made 400 nm to 1.5  $\mu\text{m}$ . In this embodiment, such a structure is formed that a silicon oxide film with a thickness of 800 nm is laminated on a silicon nitride oxide film with a thickness of 200 nm.

**[0084]** In addition, heat treatment is performed for 1 to 12 hours at 300 to 450°C in an atmosphere containing between 3 and 100% hydrogen, performing hydrogenation. This process is one of hydrogen termination of dangling bonds in the semiconductor film by hydrogen which is thermally activated. Plasma hydrogenation (using hydrogen activated by a plasma) may also be performed as another means of hydrogenation.

**[0085]** Note that the hydrogenation step may also be inserted during the formation of the first interlayer insulating film 336. Namely, hydrogen processing may be performed as above after forming the 200 nm thick silicon nitride oxide film, and then the remaining 800 nm thick silicon oxide film may be formed.

**[0086]** Next, a contact hole is formed in the first interlayer insulating film 336, and source wiring lines 337 to 340 and drain wiring lines 341 to 343 are formed. In this embodiment, this electrode is made of a laminate film of three-layer structure in which a titanium film having a thickness of 100 nm, an aluminum film containing titanium and having a thickness of 300 nm, and a titanium film having a thickness of 150 nm are continuously formed by a sputtering method. Of course, other conductive films may be used.

**[0087]** A first passivation film 344 is formed next with a thickness of 50 to 500 nm (typically between 200 and 300 nm). A 300 nm thick silicon nitride oxide film is used as the first passivation film 344 in embodiment 1. This may also be substituted by a silicon nitride film.

**[0088]** Note that it is effective to perform plasma processing using a gas containing hydrogen such as  $\text{H}_2$  or  $\text{NH}_3$  etc., before the formation of the silicon nitride oxide film. Hydrogen activated by this preprocess is supplied to the first interlayer insulating film 336, and the film quality of the first passivation film 344 is improved by performing heat treatment. At the same time, the hydrogen added to the first interlayer insulating film 336 diffuses to the lower side, and the active layers can be hydrogenated effectively.

**[0089]** Next, as shown in Fig. 5B, a second interlayer insulating film 345 made of organic resin is formed. As the organic resin, it is possible to use polyimide, polyamide, acryl, BCB (benzocyclobutene) or the like. Especially, since the second interlayer insulating film 345 is primarily used for flattening, acryl excellent in flattening properties is preferable. In this embodiment, an acrylic film is formed to a thickness sufficient to flatten a stepped portion formed by TFTs. It is appropriate that the thickness is preferably made 1 to 5  $\mu\text{m}$  (more preferably, 2 to 4  $\mu\text{m}$ ).

**[0090]** Next, a contact hole reaching the drain wiring line 343 is formed in the second interlayer insulating

film 345 and the first passivation film 344, and a pixel electrode 346 is formed. In this embodiment, as the pixel electrode 346, an aluminum alloy film (aluminum film containing titanium of 1 wt%) with a thickness of 300 nm is formed. Note that reference numeral 347 designates an end portion of an adjacent pixel electrode.

**[0091]** Next, as shown in Fig. 5C, an alkaline compound 348 is formed. In this embodiment, a lithium fluoride film is formed by an evaporation method to a thickness of 5 nm. Then, an EL layer 349 with a thickness of 100 nm is formed thereon by a spin coating method.

**[0092]** As an EL material making the EL layer 349, polymer organic materials such as polyparaphenylene vinylene (PPV) or polyfluorene and low molecular organic materials can be cited. Specifically, as the polymer organic material showing white luminescence, which becomes an luminescent layer, materials disclosed in Japanese Patent Application Laid-Open No. Hei. 8-96959 or No. Hei. 9-63770 may be used. For example, a material obtained by dissolving PVK (polyvinylcarbazole), Bu-PBD (2-(4'-tert-butylphenyl)-5-(4'-biphenyl)-1,3,4-oxadiazole), coumarin 6, DCM1 (4-dicyanomethylene-2-methyl-6-p-dimethylaminostyryl-4H-pyrene), TPB (tetraphenylbutadiene), or Nile red in 1,2-dichloromethane may be used. At this time, it is sufficient if the film thickness is made 30 to 150 nm (preferably, 40 to 100 nm). The above example is merely an example of organic materials which can be used for the EL layer of the present invention, and does not limit the present invention.

**[0093]** Besides, as described above, there are roughly four colorizing systems, and in this embodiment, a system of forming color filters corresponding to RGB for colorization was used. As the EL layer 349, a well-known material and structure may be used. However, in the present invention, a low molecular organic material capable of emitting white light was used. Note that the color filters corresponding to RGB may be positioned above the pixel electrode on the active matrix substrate. Besides, such a structure may be adopted that another substrate is bonded to the active matrix substrate so as to seal the EL element, and the color filters are provided on that substrate. Note that the color filters are not shown for simplification.

**[0094]** Besides, it is also possible to adopt a color display system in which an EL layer of blue or blue-green luminescence and a fluorescent material (fluorescent color conversion layer: CCM) are combined, or a system in which color display is made by stacking EL layers corresponding to RGB.

**[0095]** Note that in this embodiment, although the EL layer 349 is made a single layer structure of only the above luminescent layer, as the need arises, an electron injection layer, an electron transport layer, a hole transport layer, a hole injection layer, an electron blocking layer or a hole blocking layer may be provided.

**[0096]** Next, an anode 350 having a thickness of



200 nm and made of a transparent conductive film is formed to cover the EL layer 349. In this embodiment, a film made of a compound of indium oxide and zinc oxide is formed by an evaporation method, and patterning is carried out to make an anode.

**[0097]** Finally, by a plasma CVD method, a second passivation film 351 made of a silicon nitride film is formed to a thickness of 100 nm. This second passivation film 351 protects the EL layer 349 against moisture or the like. Besides, it also serves to release heat generated in the EL layer 349. In order to further raise the heat dissipation effect, it is also effective to laminate a silicon nitride film and a carbon film (preferably a diamond-like carbon film) to make a second passivation film.

**[0098]** In this way, an active matrix EL display device having a structure as shown in Fig. 5C is completed. In the active matrix EL display device of this embodiment, a TFT having an optimum structure is disposed in not only the pixel portion but also the driving circuit portion, so that very high reliability is obtained and operation characteristics can also be improved.

**[0099]** First, a TFT having a structure to decrease hot carrier injection so as not to drop the operation speed thereof as much as possible is used as an n-channel TFT of a CMOS circuit forming a driving circuit. Note that the driving circuit here includes a shift register, a buffer, a level shifter, a sampling circuit (sample and hold circuit) and the like. In the case where digital driving is made, a signal conversion circuit such as a D/A converter can also be included.

**[0100]** In the case of this embodiment, as shown in Fig. 5C, the active layer of the n-channel TFT 205 includes a source region 355, a drain region 356, an LDD region 357 and a channel formation region 358, and the LDD region 357 overlaps with the gate electrode 313, putting the gate insulating film 311 therebetween.

**[0101]** Consideration not to drop the operation speed is the reason why the LDD region is formed at only the drain region side. In this n-channel TFT, it is not necessary to pay much attention to an off current value, rather, it is better to give importance to an operation speed. Thus, it is desirable that the LDD region 357 is made to completely overlap with the gate electrode to decrease a resistance component to a minimum. That is, it is preferable to remove the so-called offset.

**[0102]** Besides, since deterioration due to hot carrier injection hardly becomes noticeable in the p-channel TFT of the CMOS circuit, an LDD region does not need to be particularly provided. Of course, it is also possible to provide an LDD region similar to the n-channel TFT to take a hot carrier countermeasure.

**[0103]** Note that, among the driving circuits, the sampling circuit is somewhat unique compared with the other sampling circuits, in that a large electric current flows in both directions in the channel forming region. Namely, the roles of the source region and the drain

region are interchanged. In addition, it is necessary to control the value of the off current to be as small as possible, and with that in mind, it is preferable to use a TFT having functions which are on an intermediate level between the switching TFT and the current control TFT in the sampling circuit.

**[0104]** The foregoing structure can be easily realized by manufacturing TFTs in accordance with the manufacturing steps shown in Figs. 3A to 5C. In this embodiment, although only the structures of the pixel portion and the driving circuit are shown, if the manufacturing steps of this embodiment are used, it is possible to form a logical circuit, such as a signal dividing circuit, a D/A converter circuit, an operational amplifier circuit, or the like other than the driving circuit on the same substrate, and further, it is believed that a memory portion, a microprocessor, or the like can be formed.

**[0105]** When the process up to Fig. 5C has been completed, a sealing material (also referred to as a housing material) 18 is formed at this time so as to surround at least the pixel portion, and preferably the driver circuits and the pixel portion (Fig. 6). Note that a plate shape material possessing a concave portion so as to surround the element portion may also be used as the sealing material 18, and that ultraviolet hardened resin may also be used. The EL element is in a state of being completely enclosed in the airtight space at this point, and is completely cutoff from the atmosphere.

**[0106]** Further, it is desirable to fill a gap 20 between the sealing material 18 and the substrate 10 with an inert gas (argon, helium, nitrogen, etc.) or to provide a drying agent such as barium oxide in the gap. By this, it is possible to suppress deterioration of the EL element by moisture or the like.

**[0107]** Besides, after the sealing processing of the EL layer is completed, a connector (flexible printed circuit: FPC 17) for connecting a terminal extended from an element or a circuit formed on the substrate to an external signal terminal is attached so that a product is completed. Note that as shown in Fig. 6, a wiring line 26 is electrically connected to the FPC 17 through a gap (which is filled with an adhesive 19) between the sealing material 18 and the substrate 300.

**[0108]** Here, the structure of an active matrix EL display device of this embodiment will be described with reference to a top view of Fig. 7. In Fig. 7, reference numeral 300 designates a substrate; 11, a pixel portion; 12, a source side driving circuit; and 13, a gate side driving circuit. The respective driving circuits reach an FPC 17 through wiring lines 14 to 16 and are connected to an external equipment.

**[0109]** In the state as described above and shown in Fig. 7, an image can be displayed on the pixel portion by connecting the FPC 17 to a terminal of the external equipment. In the present specification, an article which comes to have a state where image display can be made by attaching an FPC is defined as an EL display device.

**[0110]** Note that in this embodiment, although there is shown an example in which output light of the EL element is outputted at the upper surface side of the active matrix substrate, such a structure may be adopted that an EL element is formed of a pixel electrode (anode) made of ITO / EL layer / MgAg electrode (cathode) in sequence from below. In this case, output light of the EL element is outputted to the substrate side (lower surface side of the active matrix substrate) at which a TFT is formed.

#### [Embodiment 2]

**[0111]** In the embodiment 1, the example in which the low molecular organic material giving white luminescence is used as the EL material constituting the EL layer, has been described. In this embodiment, an example in which three kinds of polymer organic material layers corresponding to R (red), G (green) and B (blue) are stacked, will be described. Note that since this embodiment is different from the embodiment 1 only in the EL materials, only that point will be described.

**[0112]** Instead of the low molecular organic material set forth in the embodiment 1, polymer organic materials (polyparaphenylene vinylene (PPV), polyfluorene, etc.) may be used. For example, cyanopolyphenylenevinylene was used for a red luminescent material, polyphenylenevinylene was used for a green luminescent material, and polyphenylenevinylene and polyalkylphenylene were used for a blue luminescent material.

**[0113]** By adopting the structure like this, luminescence (red luminescence, green luminescence, and blue luminescence) with high luminescent brightness can be obtained.

#### [Embodiment 3]

**[0114]** Laser crystallization is used as the means of forming the crystalline silicon film 302 in embodiment 1, and a case of using a different means of crystallization is explained in embodiment 3.

**[0115]** After forming an amorphous silicon film in embodiment 3, crystallization is performed using the technique recorded in Japanese Patent Application Laid-open No. Hei 7-130652. The technique described in the above patent application is one of obtaining a crystalline silicon film having good crystallinity by using an element such as nickel as a catalyst for promoting crystallization.

**[0116]** Further, after the crystallization process is completed, a process of removing the catalyst used in the crystallization may be performed. In this case, the catalyst may be gettered using the technique recorded in Japanese Patent Application Laid-open No. Hei 10-270363 or Japanese Patent Application Laid-open No. Hei 8-330602.

**[0117]** In addition, a TFT may be formed using the

technique recorded in the specification of Japanese Patent Application No. Hei 11-076967 by the applicant of the present invention.

**[0118]** The processes of manufacturing shown in embodiment 1 are one embodiment of the present invention, and provided that the structure of Fig. 5C of embodiment 1 can be realized, then other manufacturing process may also be used without any problems, as above. Note that it is possible to freely combine the constitution of embodiment 3 with the constitution of embodiment 2.

#### [Embodiment 4]

**[0119]** Cases of using top gate type TFTs were explained by embodiment 1, but the present invention is not limited to a TFT structure, and it may also be implemented using a bottom gate type TFT (typically an inverted stagger type TFT). Further, the inverted stagger type TFT may be formed by any means.

**[0120]** The inverted stagger type TFT is a good structure having fewer processes than the top gate type TFT, and it is therefore extremely advantageous in lowering manufacturing costs, an object of the present invention. Note that it is possible to freely combine the constitution of embodiment 4 with the constitution of embodiment 2 and embodiment 3.

#### [Embodiment 5]

**[0121]** The EL display device fabricated in accordance with the present invention is of the self-emission type, and thus exhibits more excellent recognizability of the displayed image in a light place as compared to the liquid crystal display device. Furthermore, the EL display device has a wider viewing angle. Accordingly, the EL display device can be applied to a display portion in various electronic devices. For example, in order to view a TV program or the like on a large-sized screen, the EL display device in accordance with the present invention can be used as a display portion of an EL display (i.e., a display in which an EL display device is installed into a frame) having a diagonal size of 30 inches or larger (typically 40 inches or larger.)

**[0122]** The EL display includes all kinds of displays to be used for displaying information, such as a display for a personal computer, a display for receiving a TV broadcasting program, a display for advertisement display. Moreover, the EL display device in accordance with the present invention can be used as a display portion of other various electric devices.

**[0123]** Such electronic devices include a video camera, a digital camera, a goggles-type display (head mount display), a car navigation system, a car audio equipment, note-size personal computer, a game machine, a portable information terminal (a mobile computer, a portable telephone, a portable game machine, an electronic book, or the like), an image reproduction

apparatus including a recording medium (more specifically, an apparatus which can reproduce a recording medium such as a compact disc (CD), a laser disc (LD), a digital video disc (DVD), and includes a display for displaying the reproduced image), or the like. In particular, in the case of the portable information terminal, use of the EL display device is preferable, since the portable information terminal that is likely to be viewed from a tilted direction is often required to have a wide viewing angle. Figs. 8A to 8F respectively show various specific examples of such electronic devices.

**[0124]** Fig. 8A illustrates an EL display which includes a frame 2001, a support table 2002, a display portion 2003, or the like. The present invention is applicable to the display portion 2003. The EL display is of the self-emission type and therefore requires no back light. Thus, the display portion thereof can have a thickness thinner than that of the liquid crystal display device.

**[0125]** Fig. 8B illustrates a video camera which includes a main body 2101, a display portion 2102, an audio input portion 2103, operation switches 2104, a battery 2105, an image receiving portion 2106, or the like. The EL display device in accordance with the present invention can be used as the display portion 2102.

**[0126]** Fig. 8C illustrates a portion (the right-half piece) of an EL display of head mount type, which includes a main body 2201, signal cables 2202, a head mount band 2203, a display portion 2204, an optical system 2205, an EL display device 2206, or the like. The present invention is applicable to the EL display device 2206.

**[0127]** Fig. 8D illustrates an image reproduction apparatus including a recording medium (more specifically, a DVD reproduction apparatus), which includes a main body 2301, a recording medium (a CD, an LD, a DVD or the like) 2302, operation switches 2303, a display portion (a) 2304, another display portion (b) 2305, or the like. The display portion (a) is used mainly for displaying image information, while the display portion (b) is used mainly for displaying character information. The EL display device in accordance with the present invention can be used as these display portions (a) and (b). The image reproduction apparatus including a recording medium further includes a CD reproduction apparatus, a game machine or the like.

**[0128]** Fig. 8E illustrates a portable (mobile) computer which includes a main body 2401, a camera portion 2402, an image receiving portion 2403, operation switches 2404, a display portion 2405, or the like. The EL display device in accordance with the present invention can be used as the display portion 2405.

**[0129]** Fig. 8F illustrates a personal computer which includes a main body 2501, a frame 2502, a display portion 2503, a keyboard 2504, or the like. The EL display device in accordance with the present invention can be used as the display portion 2503.

**[0130]** When the brighter luminance of light emitted from the EL material becomes available in the future, the EL display device in accordance with the present invention will be applicable to a front-type or rear-type projector in which light including output image information is enlarged by means of lenses or the like to be projected.

**[0131]** A portion of the EL display device that is emitting light consumes power, so it is desirable to display information in such a manner that the light emitting portion therein becomes as small as possible. Accordingly, when the EL display device is applied to a display portion which mainly displays character information, e.g., a display portion of a portable information terminal, and more particular, a portable telephone or a car audio equipment, it is desirable to drive the EL display device so that the character information is formed by a light-emitting portion while a non-emission portion corresponds to the background.

**[0132]** With now reference to Fig. 9A, a portable telephone is illustrated, which includes a main body 2601, an audio output portion 2602, an audio input portion 2603, a display portion 2604, operation switches 2605, and an antenna 2606. The EL display device in accordance with the present invention can be used as the display portion 2604. The display portion 2604 can reduce power consumption of the portable telephone by displaying white-colored characters on a black-colored background.

**[0133]** Fig. 9B illustrates a car audio equipment which includes a main body 2701, a display portion 2702, and operation switches 2703 and 2704. The EL display device in accordance with the present invention can be used as the display portion 2702. Although the car audio equipment of the mount type is shown in the present embodiment, the present invention is also applicable to a car audio of the set type. The display portion 2702 can reduce power consumption by displaying white-colored characters on a black-colored background, which is particularly advantageous for the car audio of the set type.

**[0134]** As set forth above, the present invention can be applied variously to a wide range of electronic devices in all fields. The electronic device in the present embodiment can be obtained by utilizing an EL display device having the configuration in which the structures in Embodiments 1 through 4 are freely combined.

**[0135]** In the present invention, since the means for gamma ( $\gamma$ )-correcting a signal applied to the pixel of the EL display device is provided, the EL display device including the EL element emitting light with the suitably controlled luminescent brightness is fabricated.

**[0136]** Besides, by using the EL display device of the present invention as a display portion, an inexpensive electronic device with high visibility can be obtained.

**Claims**

1. An electronic device comprising:
- an EL display device including: 5
- a thin film transistor;
- a pixel electrode being electrically connected to the thin film transistor;
- an EL element with the pixel electrode as a cathode or an anode; and 10
- an insulating layer for sealing the EL element;
- an applying means for applying an analog image signal to the EL element; 15
- and
- a correcting means for gamma ( $\gamma$ )-correcting the analog image signal.
2. A device according to claim 1, further comprising:
- a memory for storing data for the gamma ( $\gamma$ )-correcting. 20
3. A device according to claim 1, further comprising:
- a color filter being formed at a position corresponding to the pixel electrode. 25
4. A device according to claim 1, wherein the EL element includes,
- a first pixel including a blue luminescent layer,
- a second pixel including a green luminescent layer, and 30
- a third pixel including a red luminescent layer.
5. A device according to claim 1, wherein the gamma ( $\gamma$ )-correcting amplifies a signal of red. 40
6. A device according to claim 1, wherein the gamma ( $\gamma$ )-correcting attenuates a signal of blue or green. 45
7. A device according to claim 1, wherein the gamma ( $\gamma$ )-correcting is independently applied for each of signals of blue, green and red. 50
8. A device according to claim 1, wherein the EL element includes a luminescent layer comprising a polymer organic material.
9. An EL display device comprising: 55
- a thin film transistor;
- a pixel electrode being electrically connected to

the thin film, transistor;

an EL element with the pixel electrode as a cathode or an anode;

an insulating layer for sealing the EL element;

an applying means for applying an analog image signal to the EL element;

and

a correcting means for gamma ( $\gamma$ )-correcting the analog image signal,

wherein the thin film transistor, the pixel electrode, the EL element, the insulating layer, the applying means and the correcting means are formed over a same substrate.

10. A device according to claim 9, further comprising:

a memory for storing data for the gamma ( $\gamma$ )-correcting.

11. The EL display device of claim 9, wherein the EL display device is used in an electronic device.

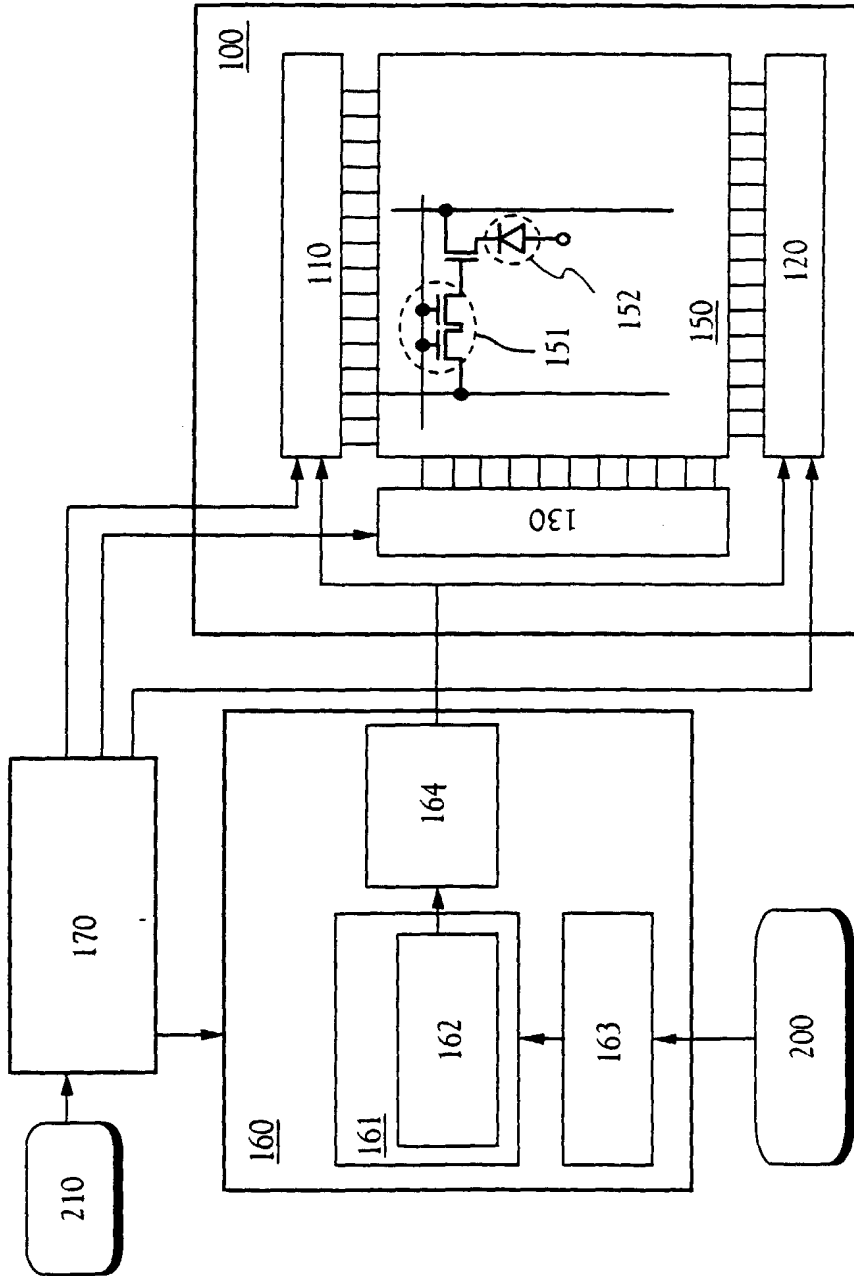


FIG. 1

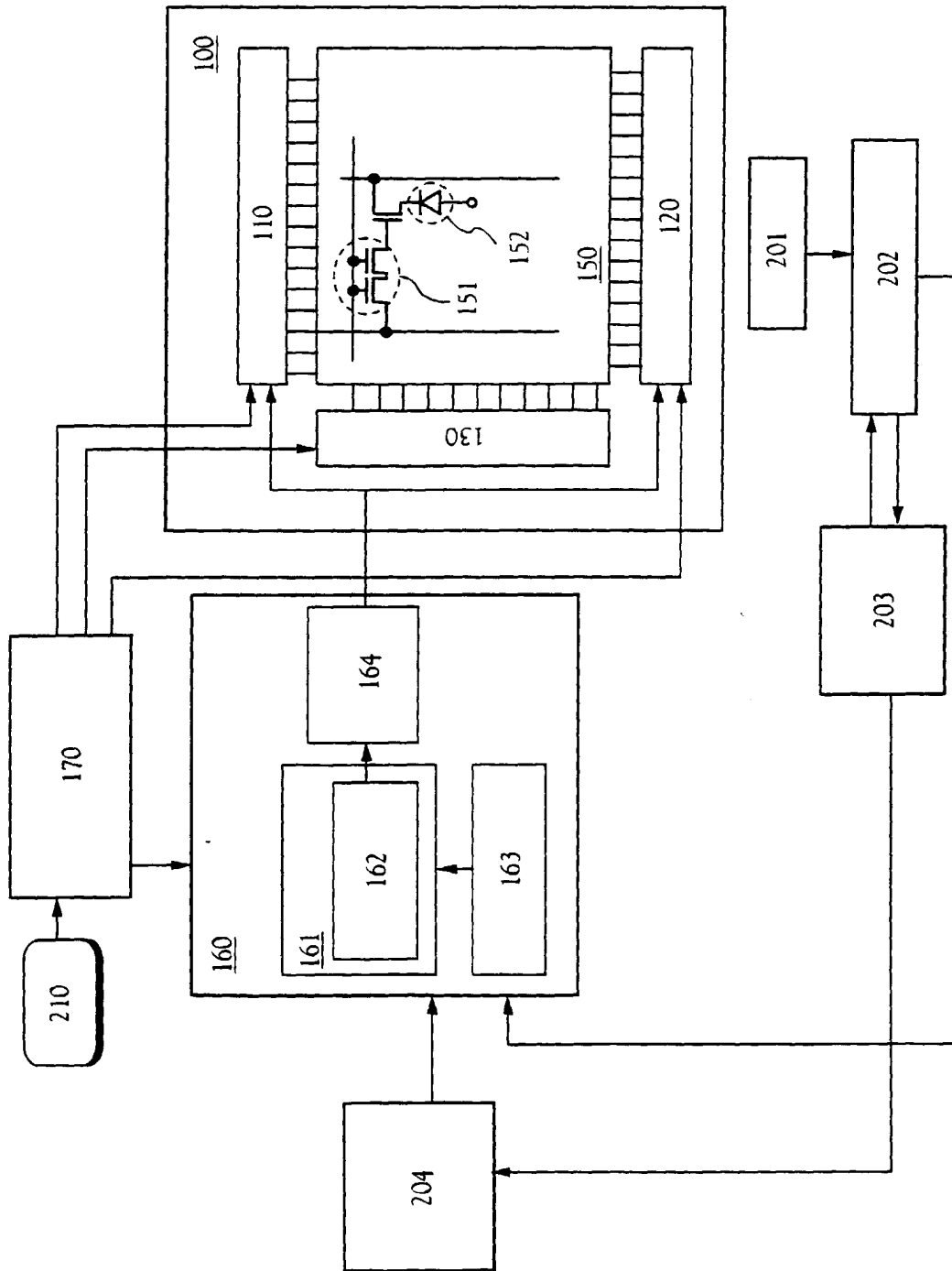
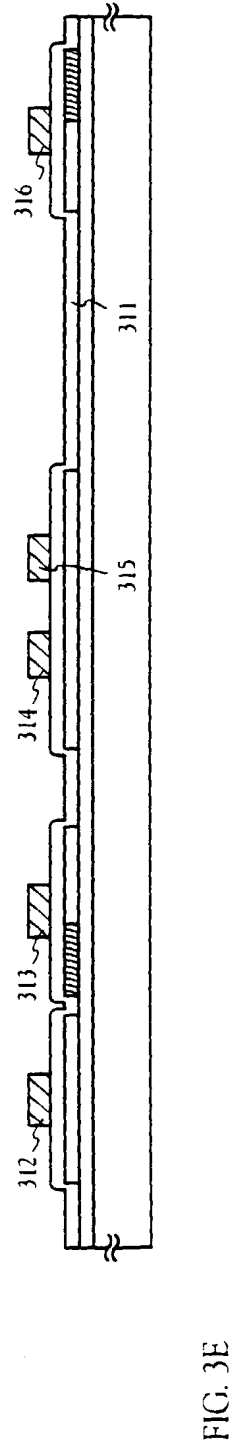
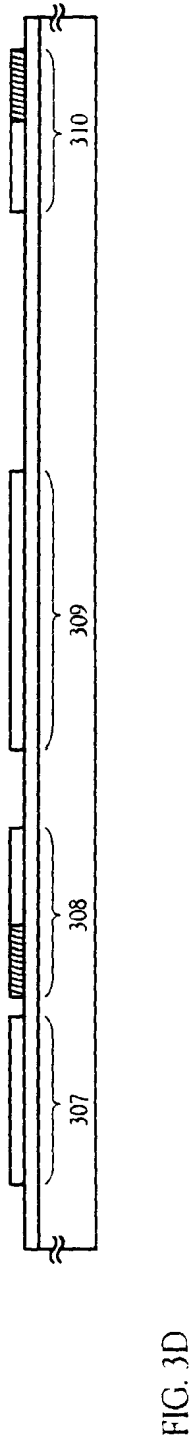
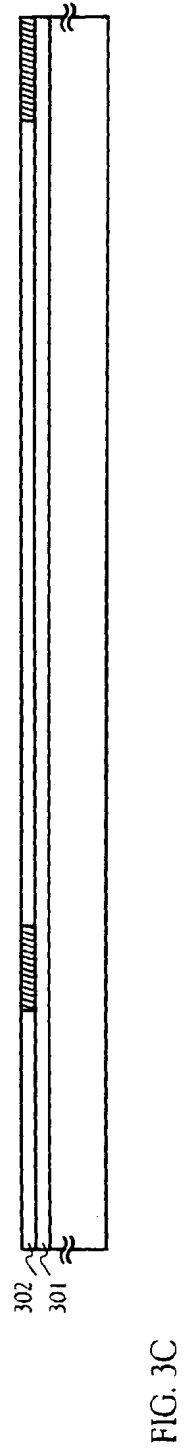
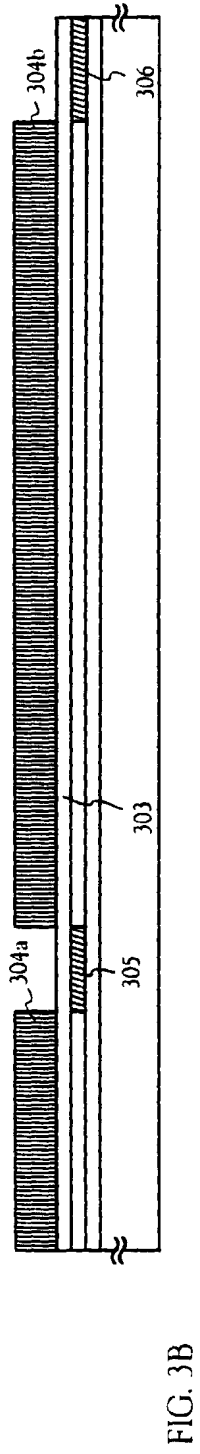
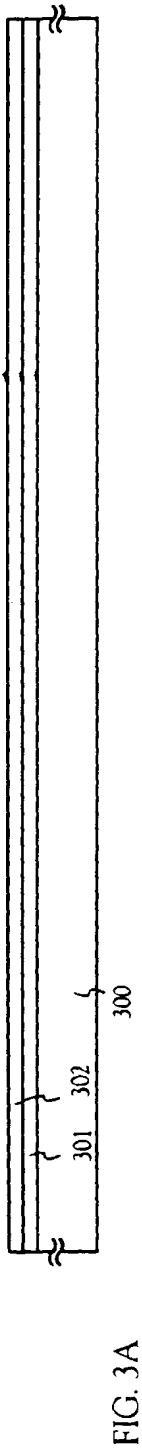


FIG. 2



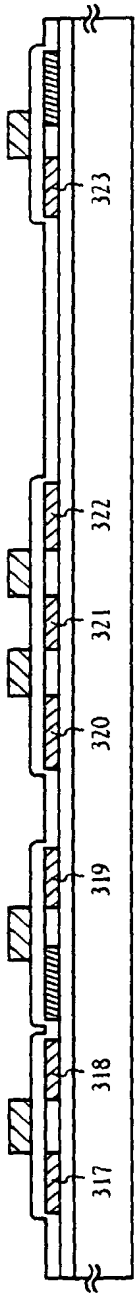


FIG. 4A

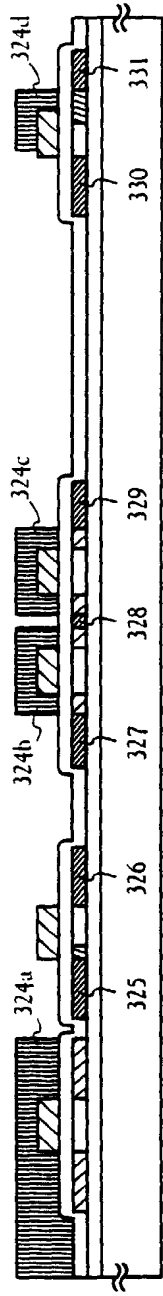


FIG. 4B

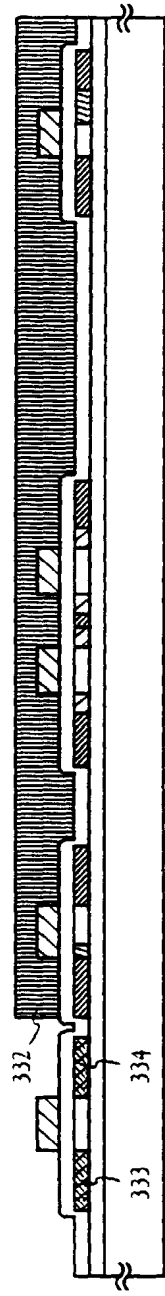


FIG. 4C

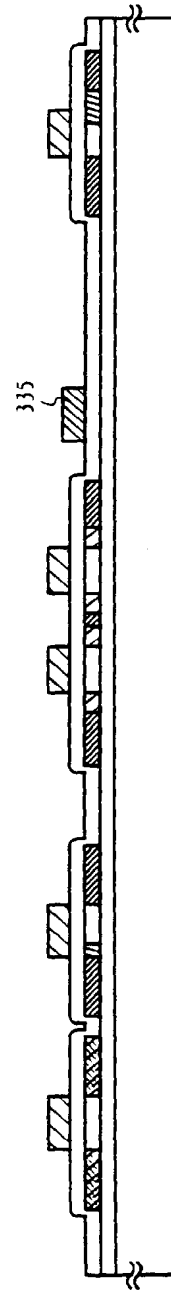


FIG. 4D



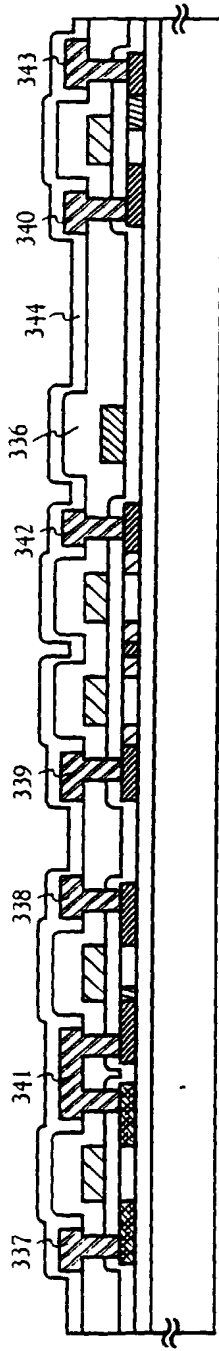


FIG. 5A

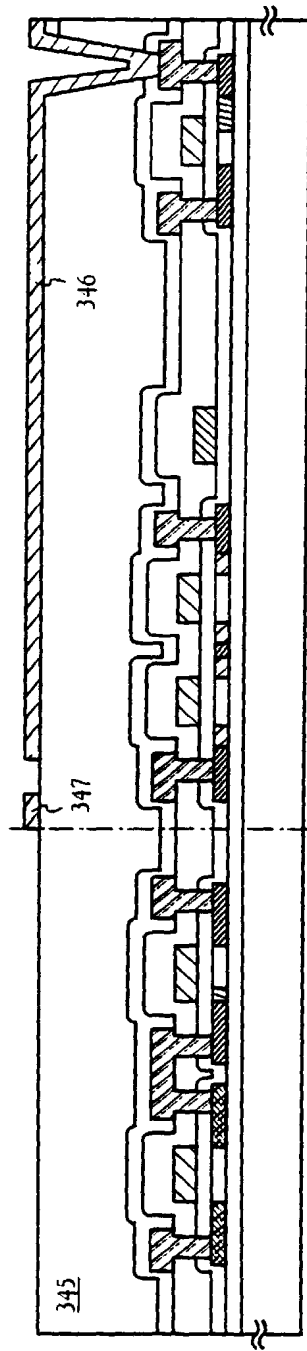


FIG. 5B

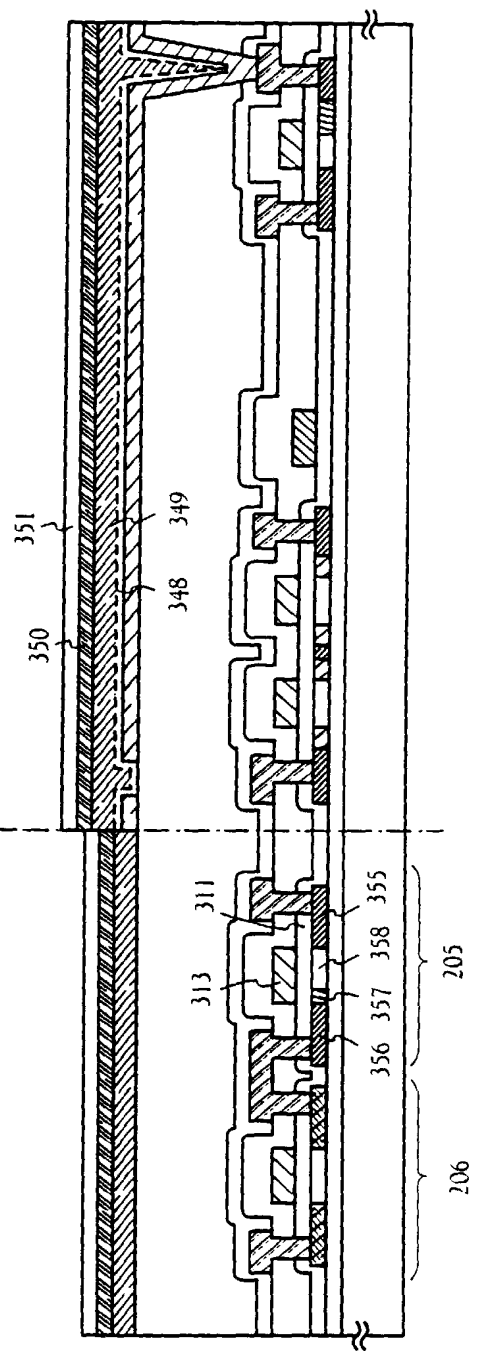


FIG. 5C

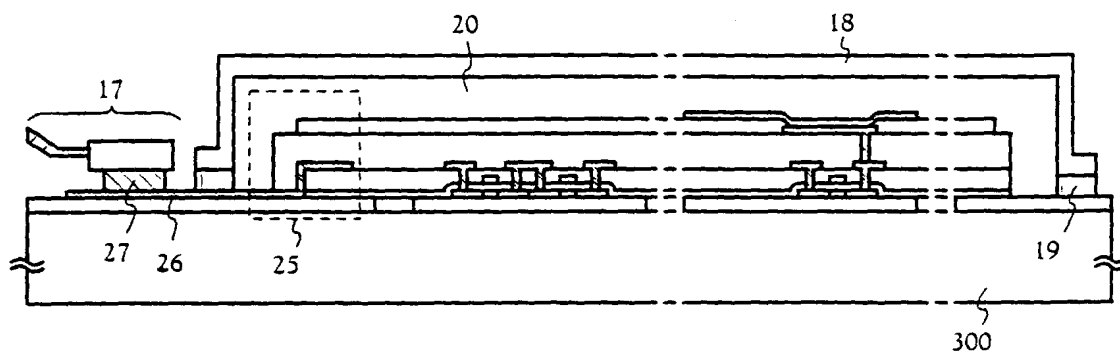


FIG. 6

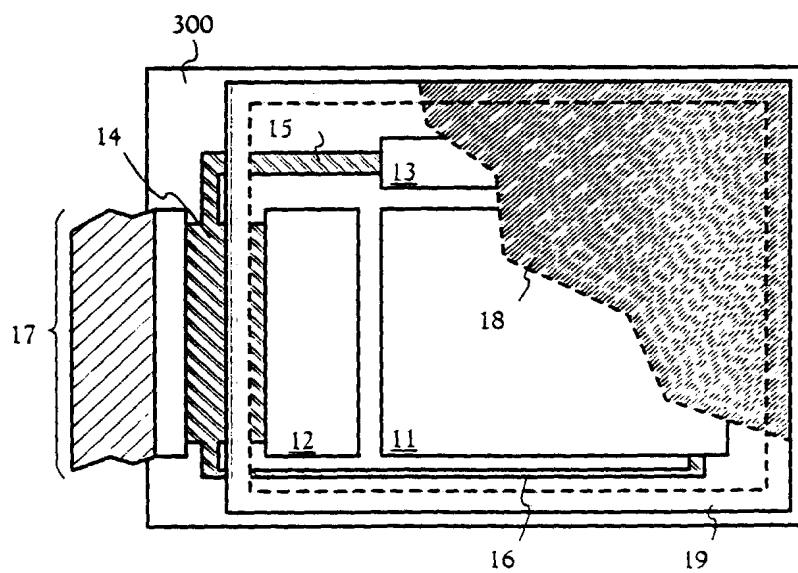


FIG. 7

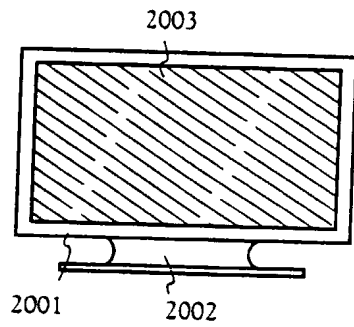


FIG. 8A

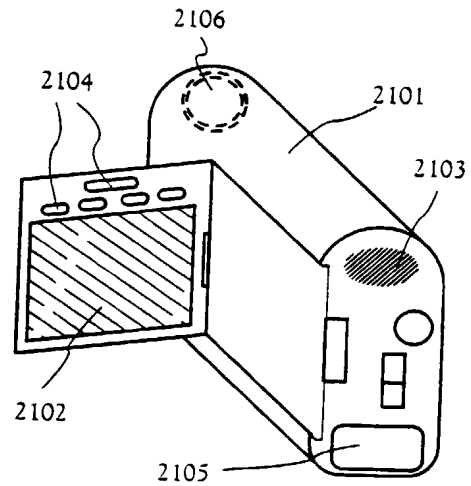


FIG. 8B

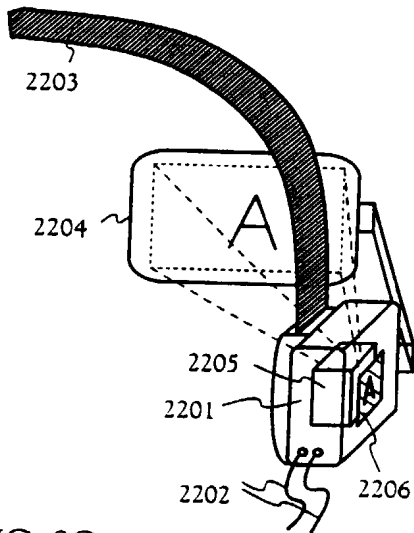


FIG. 8C

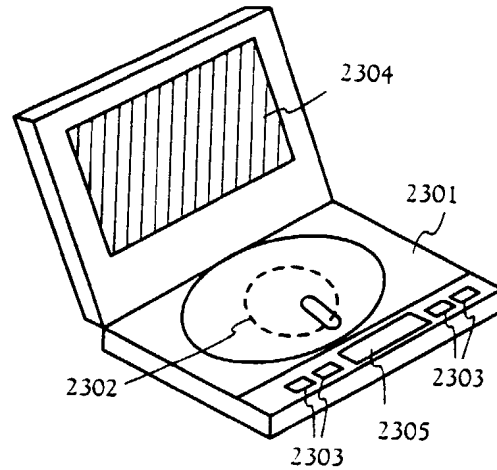


FIG. 8D

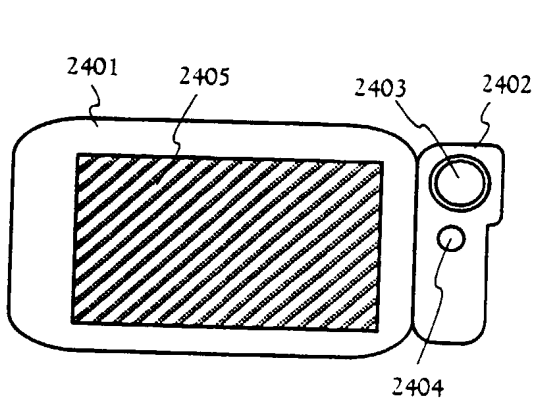


FIG. 8E

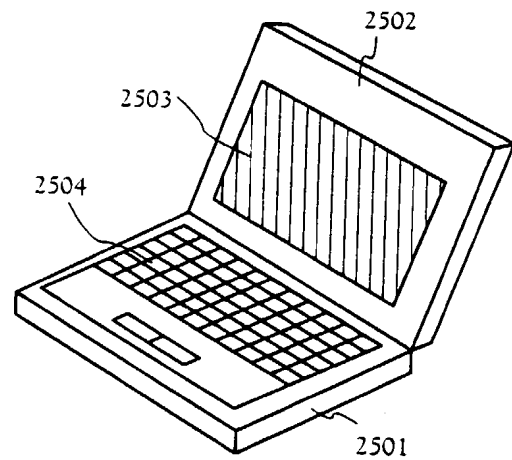


FIG. 8F

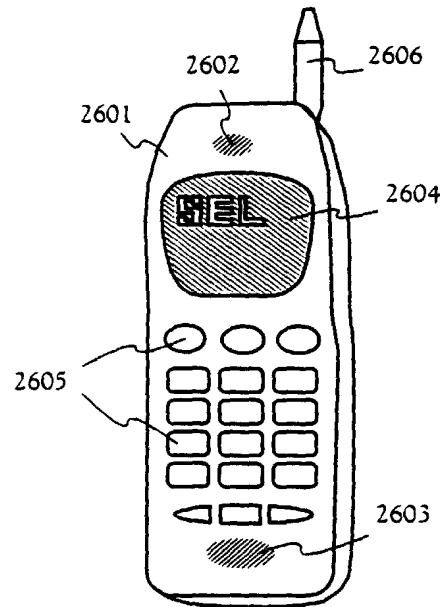


FIG. 9A

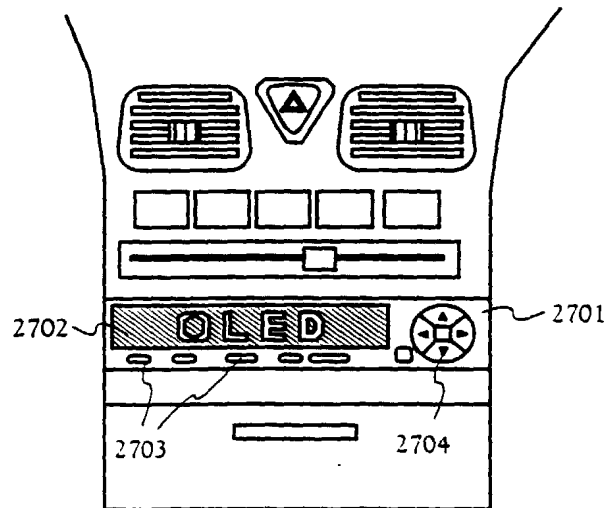


FIG. 9B

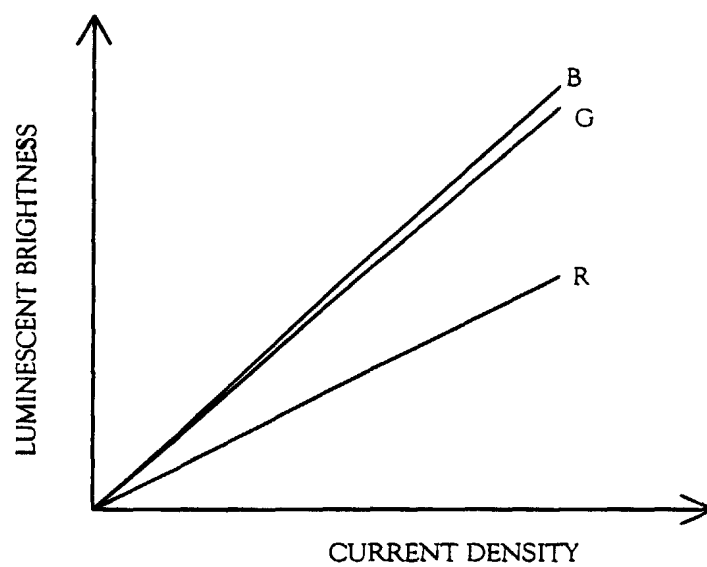


FIG. 10